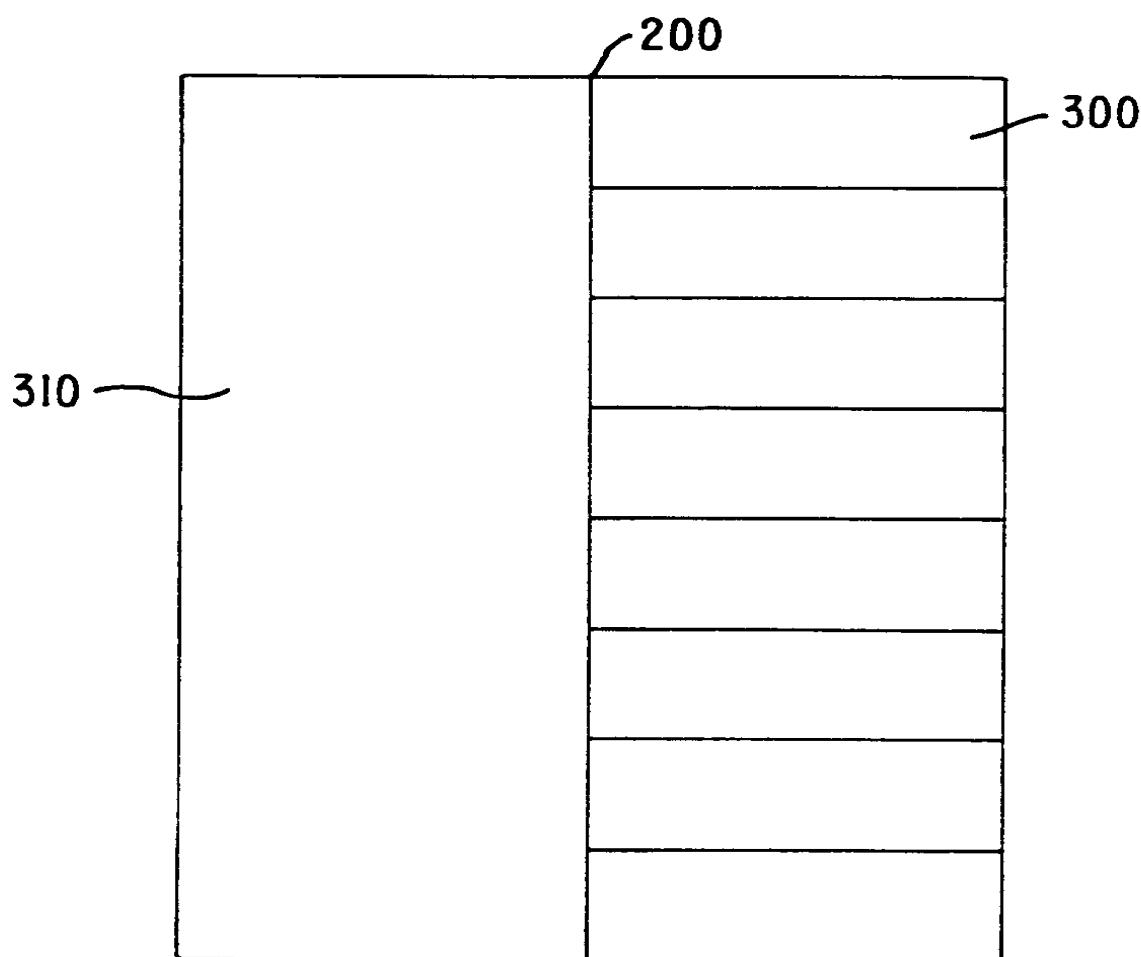


*FIG. 1.*



*FIG. 3.*

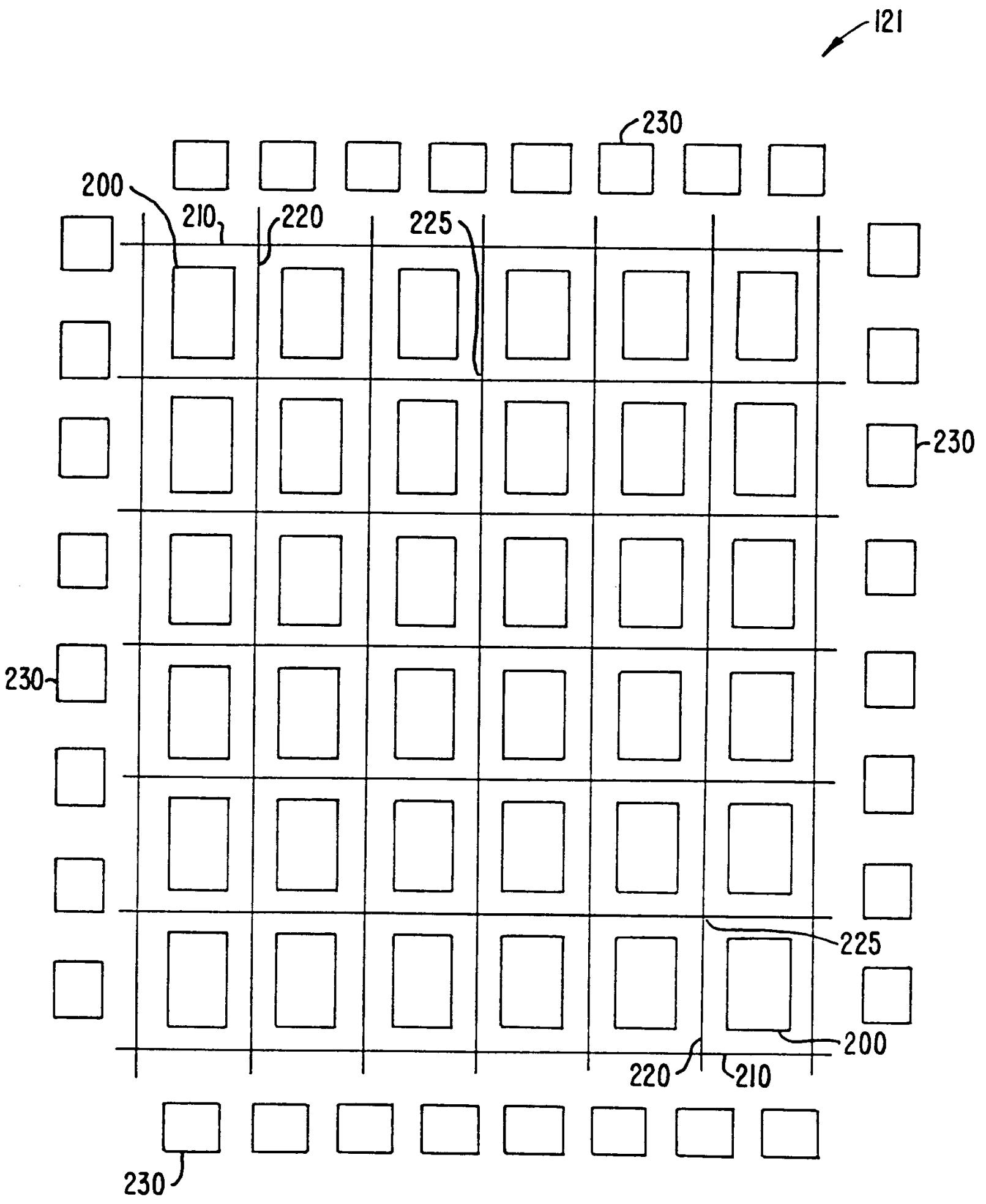
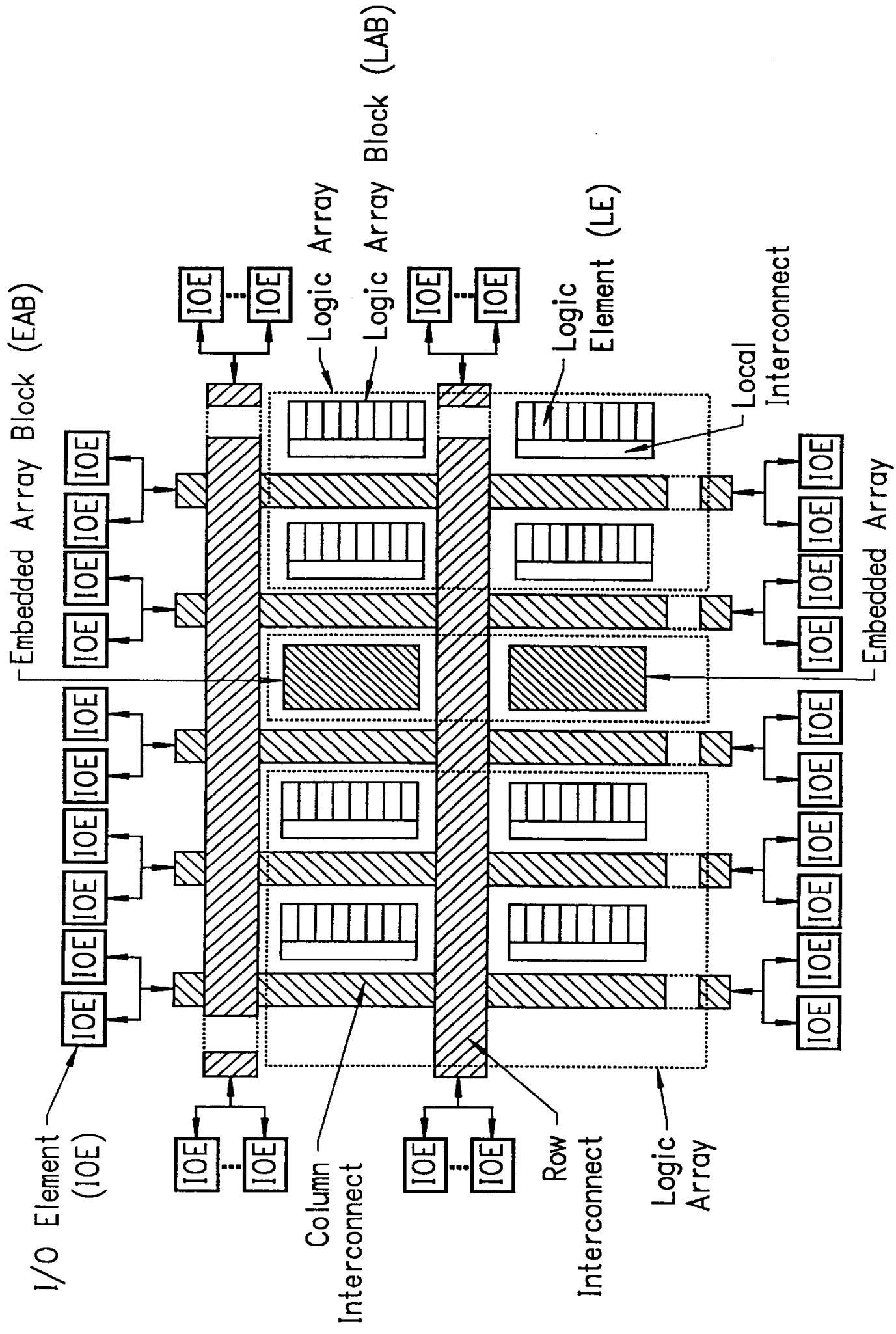


FIG. 2.

*FIG. 4*



**FIGURE 5**

# Layout note: Minimum VCCN/VSSN power bus=2 $\mu$ m; min VCCQ power = 1 $\mu$ m bus

LV/TTL/CMOS STANDARD			
NIE0	NIE1	I <sub>E0</sub>	I <sub>E1</sub>
0	0	1	1
0	1	1	0
1	0	0	1
1	1	0	0

NOT SELECTED  
NOT USED  
SCHMITT TRIGGER  
3.3V/2.5V/1.8V/1.5V STANDARD

